

10/22/2010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2195
Hong Jiang et al.	§		
	§	Examiner:	Eric Charles Wai
Serial No.: 10/750,583	§		
	§	Conf. No.:	8582
Filed: December 31, 2003	§		
	§	Docket:	ITL.1704US
For: Processing Architecture Having	§	P17510	
Passive Threads and Active	§		
Scnaphores	§	Assignee:	Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL REPLY TO FINAL REJECTION

Sir:

In response to the Advisory Action mailed July 12, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: July 29, 2010

I hereby certify that this correspondence is being electronically transmitted on the date indicated above.


Cynthia L. Hayden